

**Notice of Allowability**

Application No.

09/777,003

Examiner

Tanh Q. Nguyen

Applicant(s)

BAKER ET AL.

Art Unit

2182

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to RCE filed 06/13/06.
2. ☒ The allowed claim(s) is/are 19-44.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 06/13/06
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

### EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Greg Antrim for Joseph Sofer (Reg. No. 34,438) on August 7, 2006 to overcome minor informalities and put the application in condition for allowance.

The application has been amended as follows:

1 9. (Currently amended) An integrated multimedia system having a multimedia processor disposed in an integrated circuit, said system comprising:

a first host processor system coupled to said multimedia processor;

a second local processor disposed within said multimedia processor for controlling the operation of said multimedia processor;

a data transfer switch disposed within said multimedia processor and coupled to said second processor for transferring data to various modules of said multimedia processor, at least one of which is a data cache, wherein said data transfer switch is configured to transfer data ~~between said modules of said multimedia processor~~ in either direction between said data cache and ~~said other~~ a module within said multimedia processor as requested by said modules;

a data streamer coupled to said data transfer switch, and configured to schedule

Art Unit: 2182

simultaneous data transfers among a plurality of the various modules disposed within said multimedia processor, ~~at least one of which is a cache memory~~, in accordance with corresponding channel allocations;

an interface unit coupled to said data streamer having a plurality of input/output (I/O) device driver units;

a multiplexer coupled to said interface unit for providing access between a selected number of said I/O device driver units to external I/O devices via output pins, said I/O device driver units are provided to the interface unit directly connected to the multiplexer; and

a plurality of external I/O devices coupled to said multimedia processor.

20. (Currently amended) The system in accordance with claim 19, wherein each one of said external I/O devices ~~are~~ is controlled by a corresponding one of said I/O device driver units.

21. (Currently amended) The system in accordance with claim 20, wherein one of said external I/O devices is an NTSC decoder.

22. (Currently amended) The system in accordance with claim 20, wherein one of said external I/O devices is an NTSC encoder.

23. (Currently amended) The system in accordance with claim 20,

Art Unit: 2182

wherein one of said external I/O devices is a demodulator unit configured to demodulate wireless communications signals.

26. (Currently amended) The system in accordance with claim 20, wherein one of said external I/O devices is an ISDN interface.

27. (Currently amended) The system in accordance with claim 20, wherein one of said external I/O devices is an audio coder and decoder (CODEC) unit.

28. (Currently amended) An integrated multimedia system having a multimedia processor disposed in an integrated circuit, said system comprising:

a processor disposed within said multimedia processor for controlling the operation of said multimedia processor;

a data transfer switch disposed within said multimedia processor and coupled to said processor for transferring data to various modules of said multimedia processor, at least one of which is a data cache, wherein said data transfer switch is configured to transfer data ~~between said modules of said multimedia processor~~ in either direction between said data cache and ~~said other~~ a module within said multimedia processor as requested by said modules;

a data streamer coupled to said data transfer switch, and configured to schedule simultaneous data transfers among ~~a plurality of~~ the various modules disposed within said multimedia processor, ~~at least one of which is a cache memory,~~ in accordance with

Art Unit: 2182

corresponding channel allocations;

an interface unit coupled to said data streamer having a plurality of input/output (I/O) device driver units;

a multiplexer coupled to said interface unit for providing access between a selected number of said I/O device driver units to external I/O devices via output pins, said I/O device driver units are provided to the interface unit directly connected to the multiplexer; and

a plurality of external I/O devices coupled to said multimedia processor.

29. (Currently amended) The system in accordance with claim 28, wherein each one of said external I/O devices are is controlled by a corresponding one of said I/O device driver units.

30. (Currently amended) The system in accordance with claim 29, wherein one of said external I/O devices is an NTSC decoder.

31. (Currently amended) The system in accordance with claim 29, wherein one of said external I/O devices is an NTSC encoder.

32. (Currently amended) The system in accordance with claim 29, wherein one of said external I/O devices is a demodulator unit configured to demodulate wireless communications signals.

35. (Currently amended) The system in accordance with claim 29, wherein one of said external I/O devices is an ISDN interface.

36. (Currently amended) The system in accordance with claim 29, wherein one of said external I/O devices is an audio coder and decoder (CODEC) unit.

37. (Currently amended) The system in accordance with claim 19, ~~further comprising a cache memory~~ wherein said data cache is directly coupled to said second local processor and said data transfer switch.

38. (Currently amended) The system in accordance with claim 28, wherein said data cache ~~memory~~ is directly coupled to said processor and said data transfer switch.

41. (Currently amended) The system in accordance with claim 39, wherein said plurality of modules among which said data streamer configures to schedule simultaneous data transfers further include another interface unit which is capable of ~~controlling~~ communicating with said first processor.

41. (Currently amended) The system in accordance with claim 40, wherein said plurality of modules among which said data streamer configures to

Art Unit: 2182

schedule simultaneous data transfers further include another interface unit which is capable of ~~controlling~~ communicating with a first host processor.

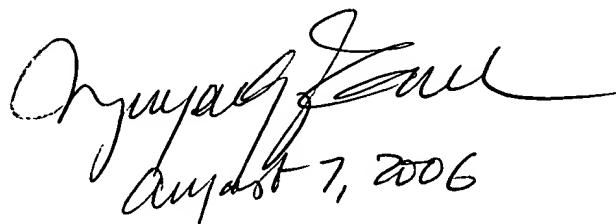
### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tanh Q. Nguyen whose telephone number is 571-272-4154. The examiner can normally be reached on M-F 9:30AM-7:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim Huynh can be reached on 571-272-4147. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

TQN  
August 7, 2006



Tanh Q. Nguyen  
August 7, 2006